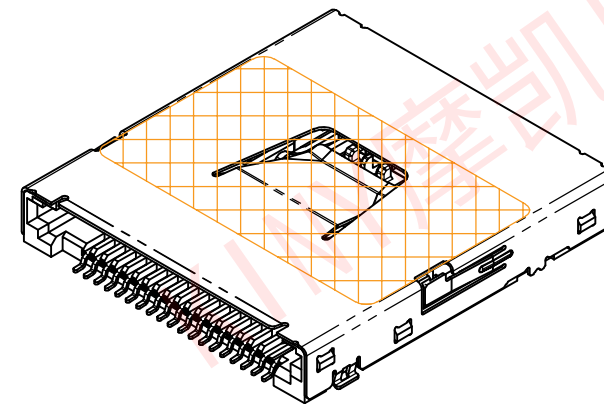
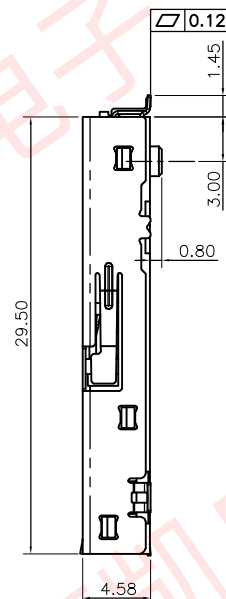
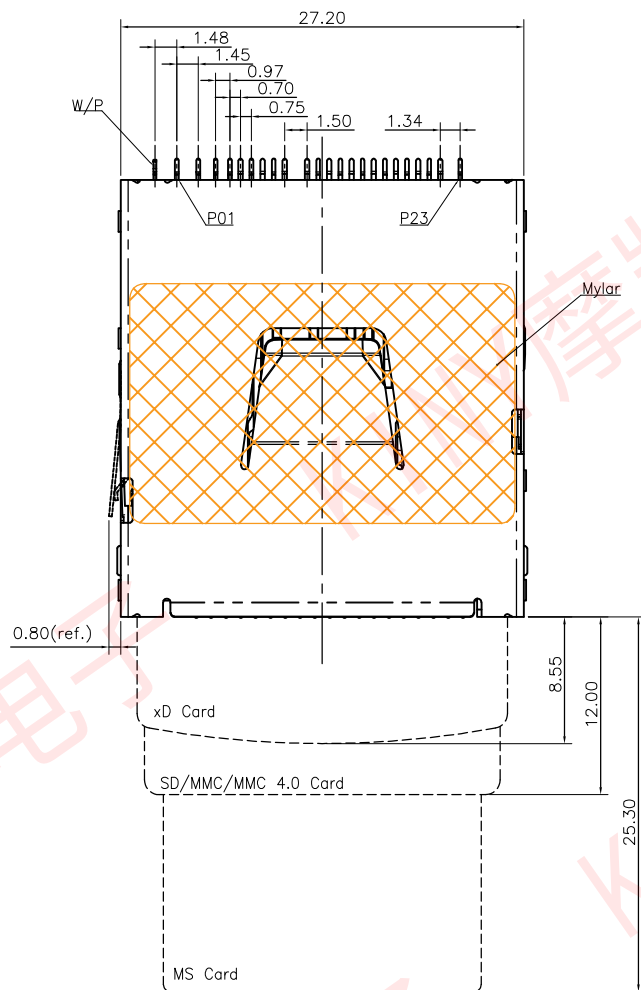


* 所有原料材质, 生产制程, 电镀必须符合HF要求



Note:

1. Material:

1-1. Plastic: LCP, S475, Black, UL94V-0

1-2. Contact: C5210-H

1-3. Shell: SUS304-H

2. Plating:

2-1. Contact:

Contact Area: Au 1 μ " min. Plating
Over Ni 50 μ " min.

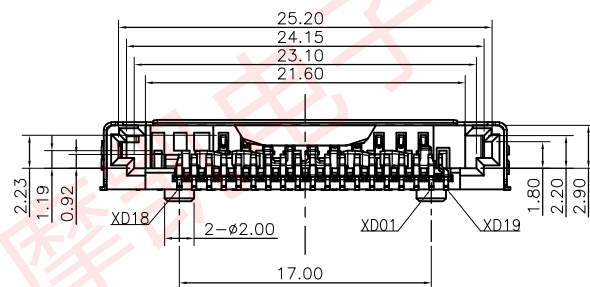
Soldertail: Matte Tin 100 μ " min. Plating
Over Ni 50 μ " min.

2-2. Shell:

50 μ " Ni Plating Overall

Soldertail: 1 μ " Gold Plating Over Ni 50 μ " min.

3. Contact And Tail Coplanarity To Be 0.12mm max



KINY

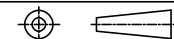
东莞市摩凯电子有限公司

DIMENSIONS INIT: mm
UNLESS OTHERWISE SPECIFIABLE
DIMENSION TOLERANCE
X.X: $\pm$ 0.35
X.XX: $\pm$ 0.20
X.XXX: $\pm$ 0.10
ANGULAR: $\pm$ 2'

PRODUCT NAME :
5 in 1 H4.60 Conn.

PRODUCT NO. :
51460-T1251-01

DRAWING NO. :
D-51460-T1251-01



DRAWING: Jack

CHECK: Alex

APPROVED: Alex

SCALE: 1:1

DWG ID: C D

REV.: A

PAGE: 1 OF 2

A	----	PRE-RELEASE	Alexander	2020.10.24
REVISION NO.	ECR NO.	DESCRIPTION	MARK	DATE

1

2

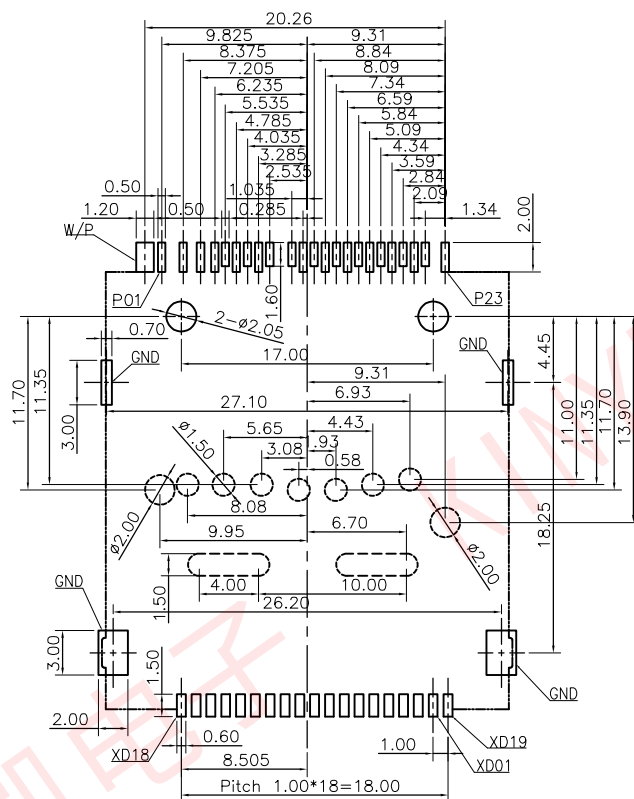
3

4

5

6

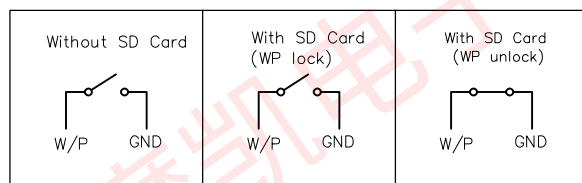
* 所有原料材质,生产制程,电镀必须符合HF要求



RECOMMEENDED PCB LAYOUT

TOLERANCE UNLESS OTHERWISE ± 0.05

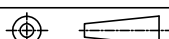
-  PAD AREA
 NO COPPER AREA
 CONN. OUTLINE



SD/MMC/MMC 4.0/MS Pin Assignment								
SD			MMC		MMC 4.0		MS	
PIN No.	Name	Description	Name	Description	Name	Description	Name	Function
W/P		Write Protect						
P01	DAT1	Data Line [Bit 1]			DAT1	Data		
P02	DAT0	Data Line [Bit 0]	DAT	Data	DAT0	Data		
P03					DAT7	Data		
P04	VSS2	Support Voltage Ground	VSS2	Support Voltage Ground	VSS2	Support Voltage Ground		
P05							VSS	VSS
P06					DAT6	Data		
P07							BS	Serial protocol bus state signal
P08	CLK	Clock	CLK	Clock	CLK	Clock		
P09							VCC	VCC
P10							SDIO	Serial protocol data signal
P11	VDD	Support Voltage	VDD	Support Voltage	VDD	Support Voltage		
P12							Reserved	Reserved
P13	VSS1	Support Voltage Ground	VSS1	Support Voltage Ground	VSS1	Support Voltage Ground		
P14							INS	Stick insertion/extraction detect
P15					DAT5	Data		
P16							Reserved	Reserved
P17	CMD	Command/Response	CMD	Command/Response	CMD	Command/Response		
P18							SCLK	Serial protocol clock signal
P19					DAT4	Data		
P20							VCC	VCC
P21	CD/DAT3	Card Detect/Data Line [Bit 3]	CD/RS	Reserved for future use	DAT3	Data		
P22							VSS	VSS
P23	DAT2	Data Line [Bit 2]			DAT2	Data		

xD Pin Assignment	
PIN No.	FUNCTION
XD01	CARD DETECT
XD02	READY / BUSY
XD03	READ ENABLE
XD04	CARD ENABLE
XD05	COMMAND LATCH ENABLE
XD06	ADDRESS LATCH ENABLE
XD07	WRITE ENABLE
XD08	WRITE PROTECT
XD09	GND
XD10	DATA0
XD11	DATA1
XD12	DATA2
XD13	DATA3
XD14	DATA4
XD15	DATA5
XD16	DATA6
XD17	DATA7
XD18	VCC
XD19	GND

A	----	PRE-RELEASE	Alexander	2020.10.24
REVISION NO.	ECR NO.	DESCRIPTION	MARK	DATE
1	2	3		

KINYO		东莞市摩凯电子有限公司							
DIMENSIONS INIT: mm UNLESS OTHERWISE SPECIFIABLE DIMENSIONTOLERANCE X.X: ±0.35 X.XX: ±0.20 X.XXX: ±0.10 ANGULAR: ±2'		PRODUCT NAME : 5 in 1 H4.60 Conn.		DRAWING: Jack		DATE: 2020.10.24			
		PRODUCT NO. : 51460-T1251-01		CHECK: Alex		DATE: 2020.10.24			
		DRAWING NO. : D-51460-T1251-01		APPROVED: Alex		DATE: 2020.10.24			
				SCALE: 1:1		DWG ID: C D		REV.: A	